

Training Opportunity for Portuguese Trainees

Reference	Title	Duty Station
PT-2020-TEC-SWT(1)	Artificial Intelligence for embedded space systems	ESTEC
<p>Overview of the Unit missions:</p> <p>The Software Systems division has the responsibility in domain of software engineering for flight and ground software systems. It is supporting all ESA satellite projects in the related technologies, covering Software engineering technologies, methods, tools, architectures, and standards. Specific interest goes into the aspect of System-software co-engineering over the entire development life-cycle, starting from requirements engineering and modelling, design methods, to automatic code and test generation, including the required languages and compilers. Modelling and Simulation is used to support system engineering and testing and verification.</p> <p>Advanced SW technologies as Artificial Intelligence, Big Data and Data Warehousing are investigated for their potential to improve future systems capabilities and their way to develop them.</p>		
<p>Overview of the field of activity proposed:</p> <p>Within the domain of Software Systems division, the Software Technology Section offer a training opportunity within Artificial Intelligence for embedded space systems:</p> <p>Artificial intelligence is more and more used in Ground space applications. One main domain of application concerns Machine Learning. In this domain, applications able to identify elements of interest on space images are already developed and new ones are being developed. These applications are mainly based on Deep Learning architectures mostly implemented by artificial neural networks.</p> <p>The training of such neural networks requires intensive processing that is largely exceeding the capabilities of embedded space systems. However, their execution is less demanding in term of processing and could now fit the most recent or coming architectures.</p> <p>Among these architectures, multi-core processors (GR740), FPGAS (BRAVE medium) and SoCs (BRAVE Large/Ultra, DAHLIA, HPDP and ZynQ) grouping processor(s) and reconfigurable FPGA.</p> <p>If Deep Neural Networks are usually considering the use of GPUs to minimize the execution time, they can be executed on generic purpose processors (full Software) with less performance. Thanks to the availability of systems including a processor and a FPGA, a HW/SW co-design approach enables a split between functions executed on the processor(s) and the ones executed on the FPGA.</p> <p>During the proposed activity, the candidate shall:</p> <ul style="list-style-type: none"> - Evaluate the existing Deep Neural Network systems, identify their needs in term of processing and select one of them. - Evaluate the new embedded computer architectures, with a special focus on SoCs and select one of them. - Follow a HW/SW co-design approach to identify the functions to be executed on the processors and the functions to be executed on the FPGA. - Apply the approach and port the selected Deep Neural Network on the selected embedded system. - Measure the performance of the Deep Neural Network. - Potentially iterate to optimize the allocation of functions. 		
<p>Required Education:</p> <p>Applicants should have just completed, or be in their final year of a University course at Masters Level (or equivalent) in a technical or scientific discipline.</p> <p>Applicants should have good interpersonal and communication skills and should be able to work in a multi-cultural environment, both independently and as part of a team.</p> <p>Applicants must be fluent in English and/or French, the working languages of the Agency.</p>		