

ESO Portuguese Traineeship Opportunities 2014

2. Electronic Department	Directorate of Engineering
Job code	ESO-DE(2)
Education, knowledge and skills requirements	Degree in Electronic Engineering
Project information	<p><i>Virtualization of an existing digital board</i></p> <ul style="list-style-type: none"> - FPGA, VHDL (ALTERA); - Uses of a multichannel signal analyser (back engineering); - Design of digital electronics based on FPGA and discrete digital components; - LabWindows CVI, LabView (test bench); - PCB layouting (ALTIUM Designer).
Learning opportunities for the Trainee	Gain the full understanding of the existing system, Proposal of an architecture, and first prototype concept, Design, manufacture and test of the first prototype, Extension of an existing bench to cope with stepper motors, Qualification of the prototype and preparation for mass production.