



# Sample Project: Design of communications integrated circuits

The candidate will work at CERN...

Code	PH1254
Programme	TRAIN-PTES
Department	PH
Responsible	30385 - Mr. Paulo Rodrigues Simoes Moreira
Created by	30385 - Mr. Paulo Rodrigues Simoes Moreira
Updated by	54482 - Ms. Sophie Baron
Date Created	30-JAN-09
Date updated	16-JUN-14

## Title

Design of communications integrated circuitsThe candidate will work at CERN...

## Description

Design of communications integrated circuits The candidate will work at CERN in the micro-electronics section for a period of two years. He/she will be involved in the development of a high speed data communications ASIC that will be used in the future SLHC experiments. The engineer will use a mix of full-custom and standard cell design techniques and will be involved on the design of variety of circuits such as phase-locked loops, serializers, clock recover circuits, high speed line drivers and on the design of digital logic. Additionally, the engineer will be also involved on evaluation testing of the ASIC.

## Skills

Networks and Systems: Communication networks, Integrated circuits, VLSI. Theory of Electrical Engineering: Modeling and simulation

## Disciplines

Electronic Engineering

To edit this project go to [https://hrapps.cern.ch/auth/f?p=131:4:::P4\\_ID:1254](https://hrapps.cern.ch/auth/f?p=131:4:::P4_ID:1254)